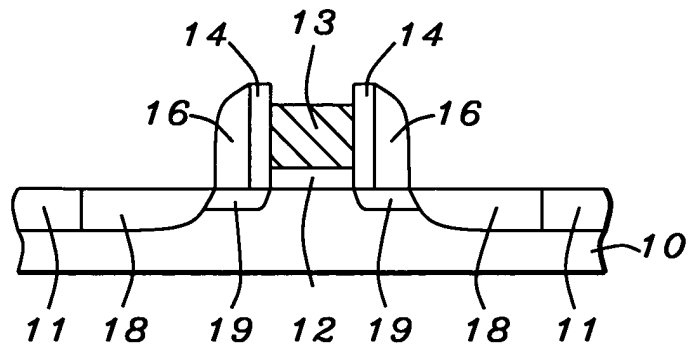
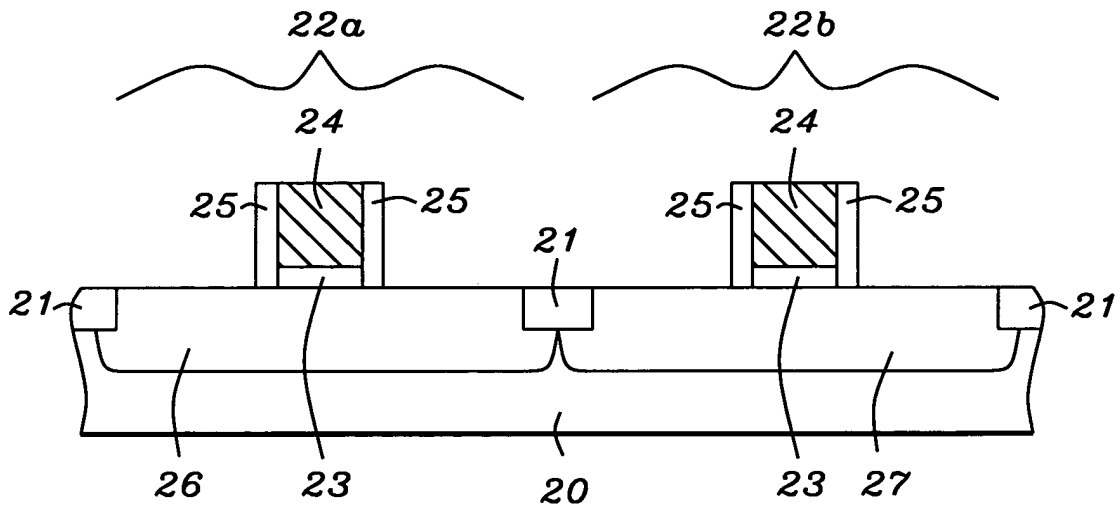


Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate 10 with a central layer 12 and side regions 11. A central block 13 is positioned on layer 12, flanked by blocks 14. Blocks 14 are on top of layer 12, and blocks 16 are on top of blocks 14. Arrows 17 point down to blocks 14.

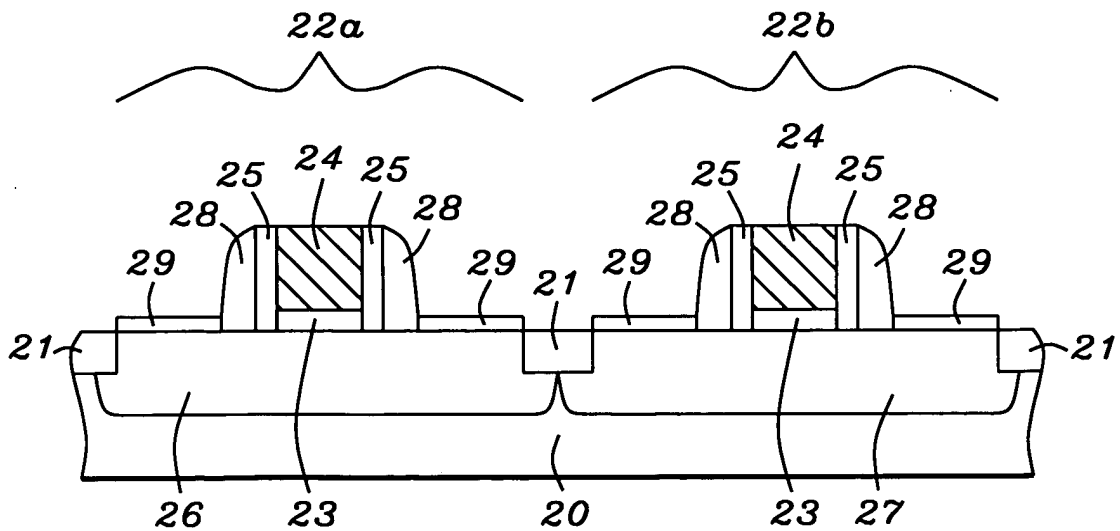
FIG. 2b - Prior Art



*FIG. 2c - Prior Art*



*FIG. 3a*



*FIG. 3b*

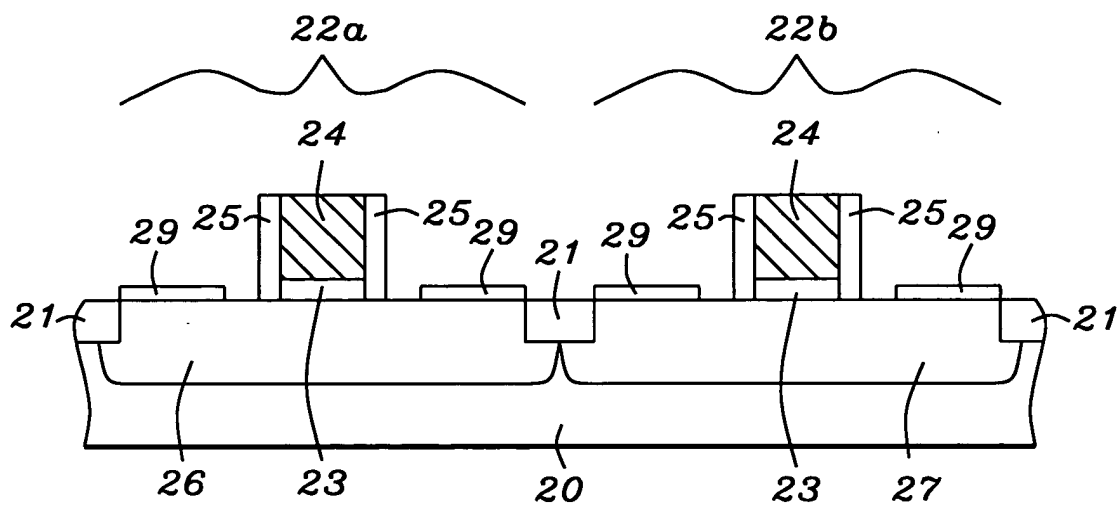


FIG. 3c

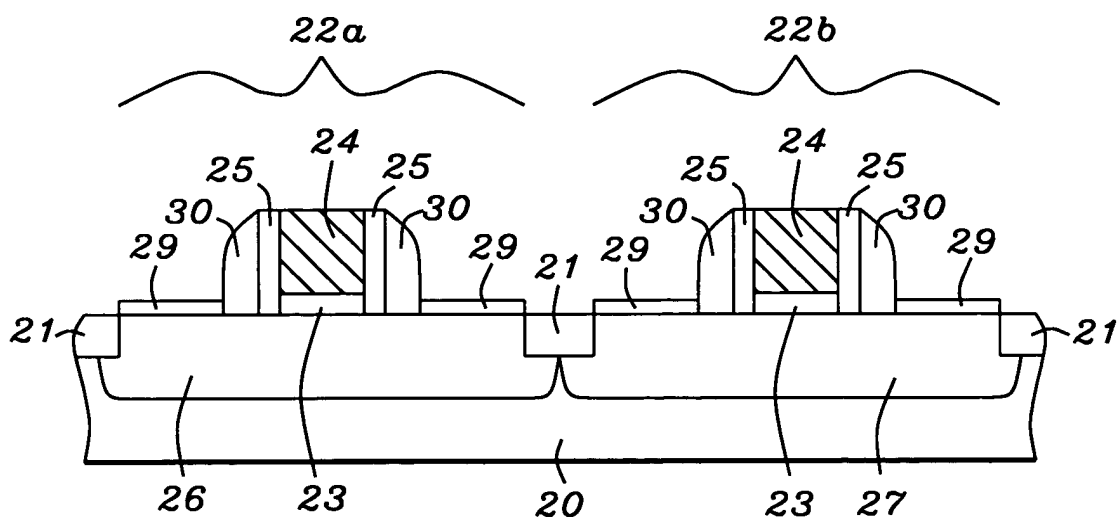


FIG. 3d

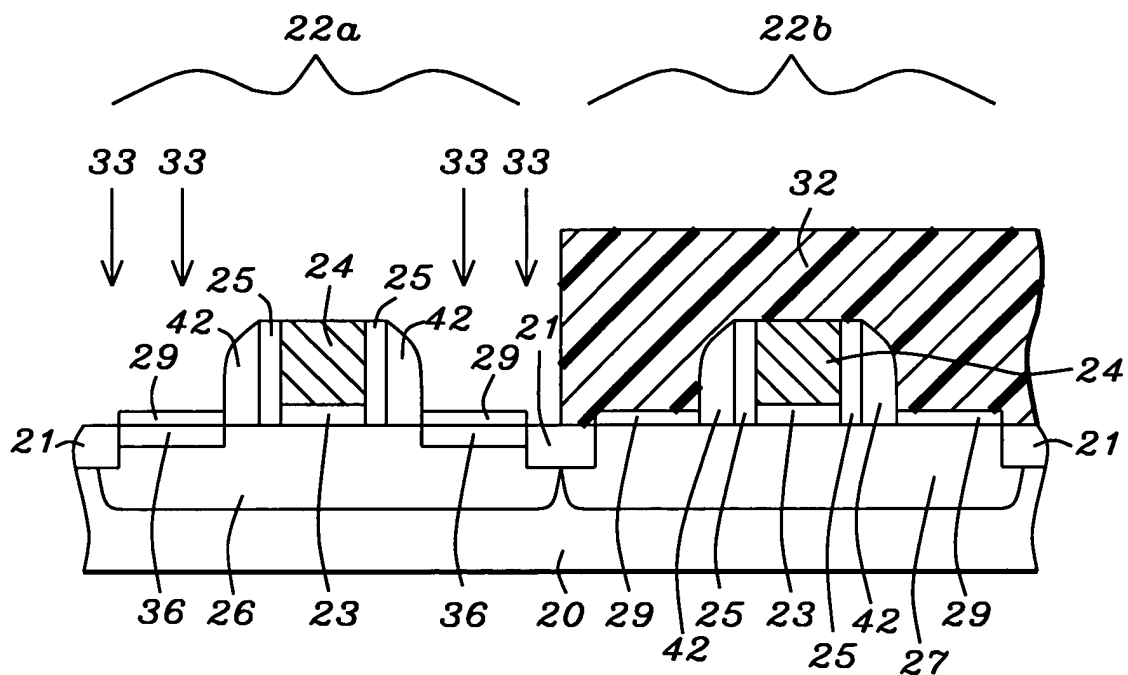


FIG. 3e

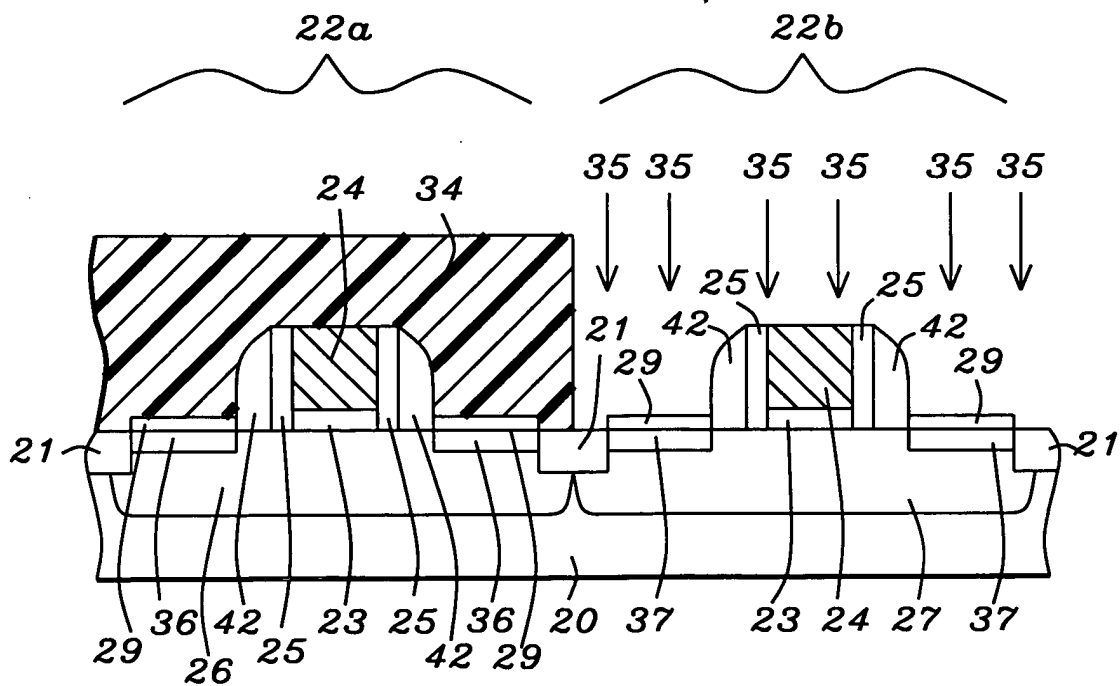


FIG. 3f

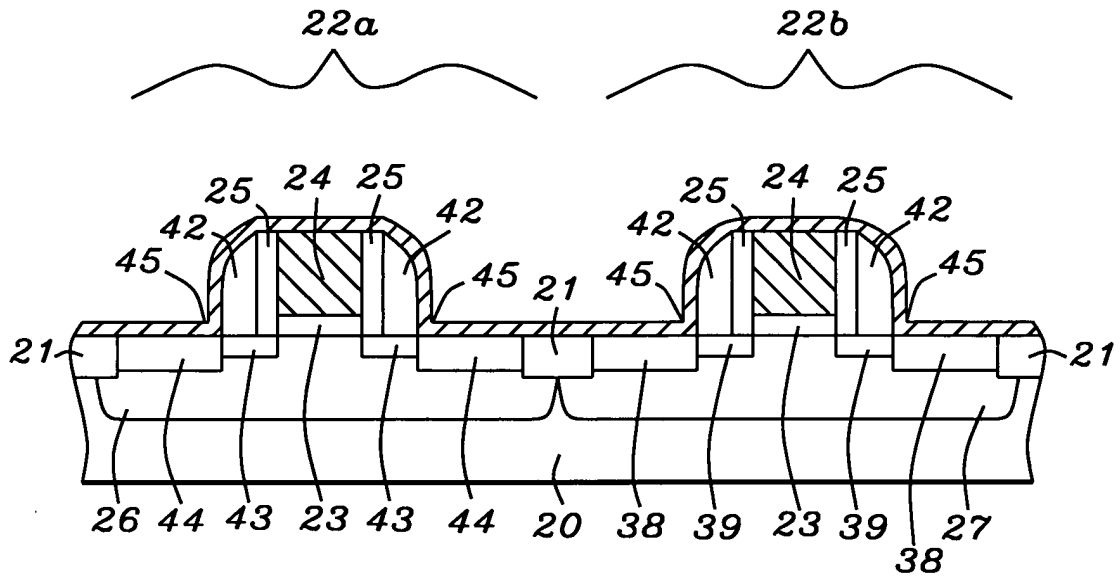


FIG. 3g

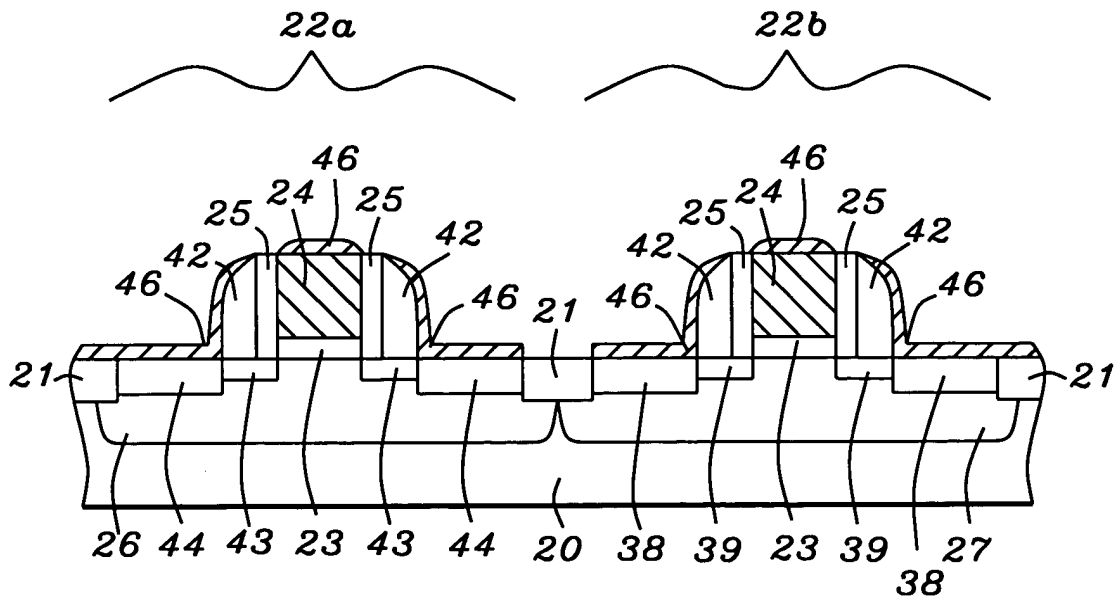


FIG. 3h

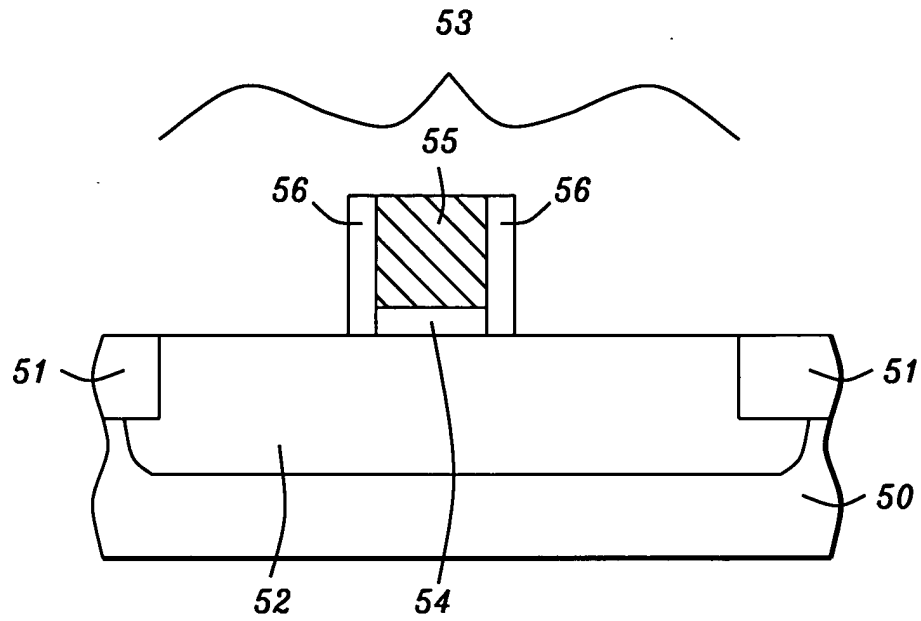


FIG. 4a

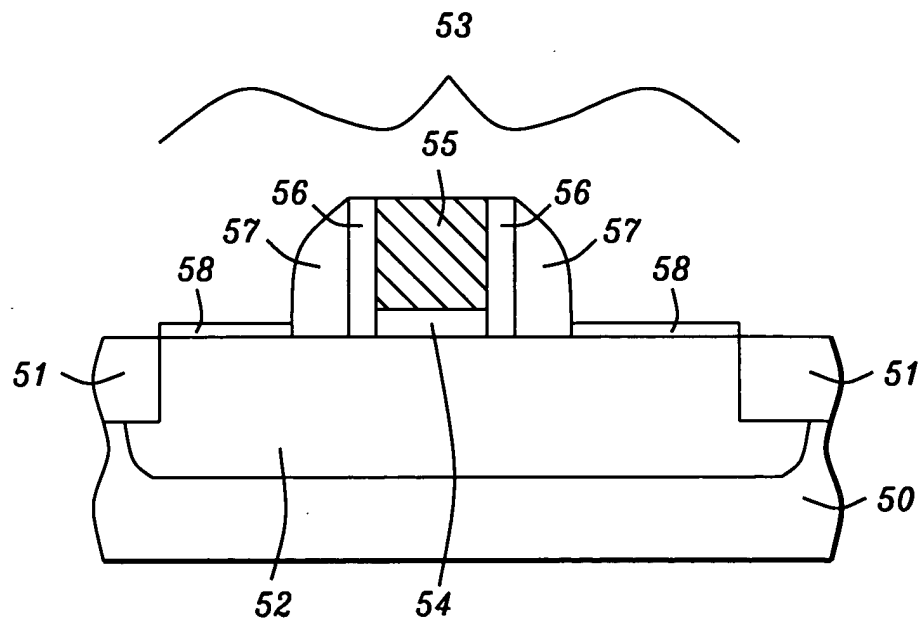


FIG. 4b

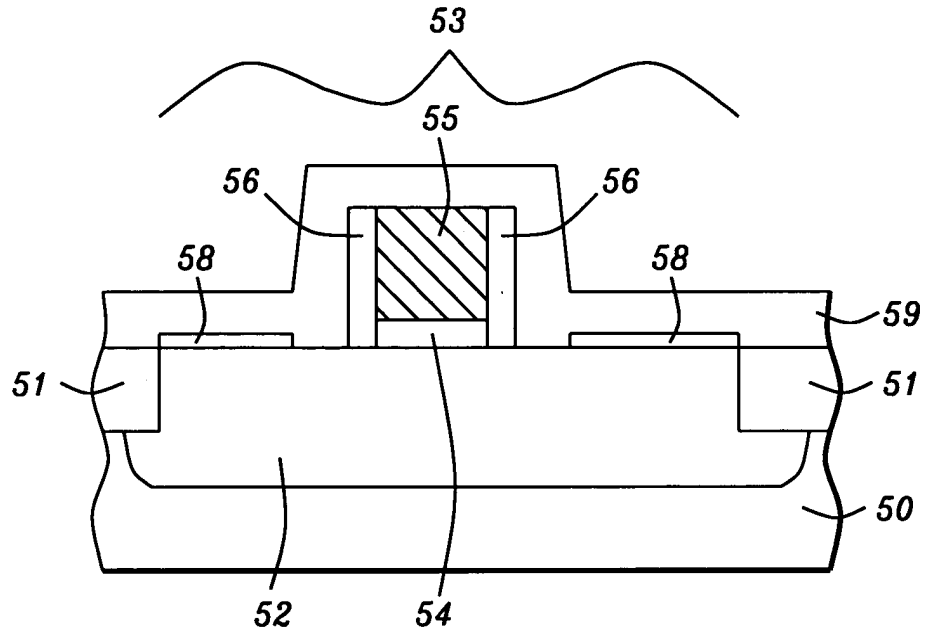


FIG. 4c

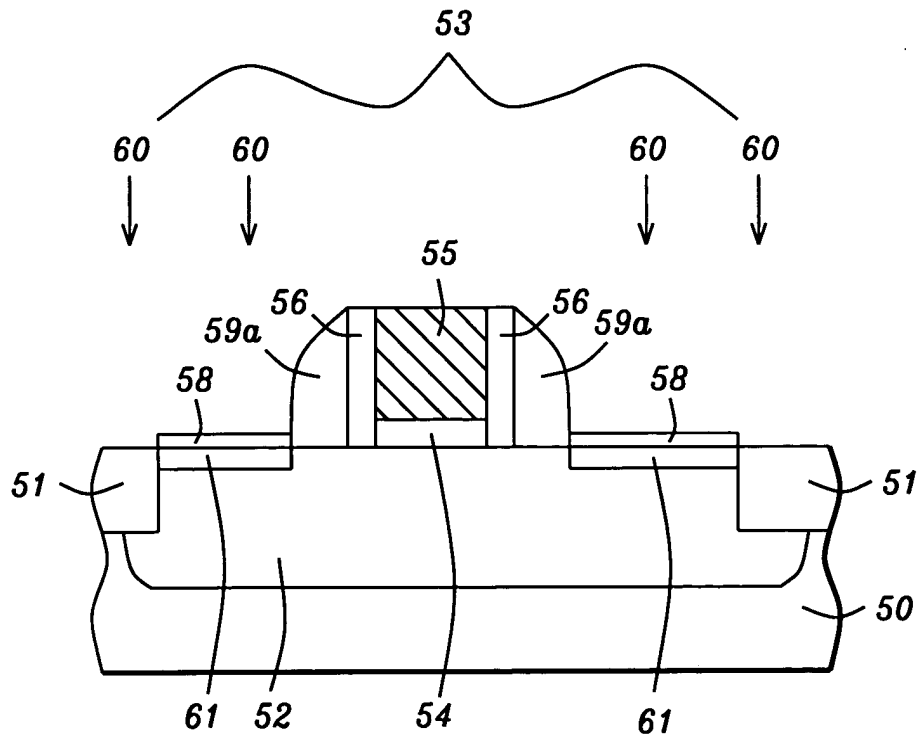


FIG. 4d

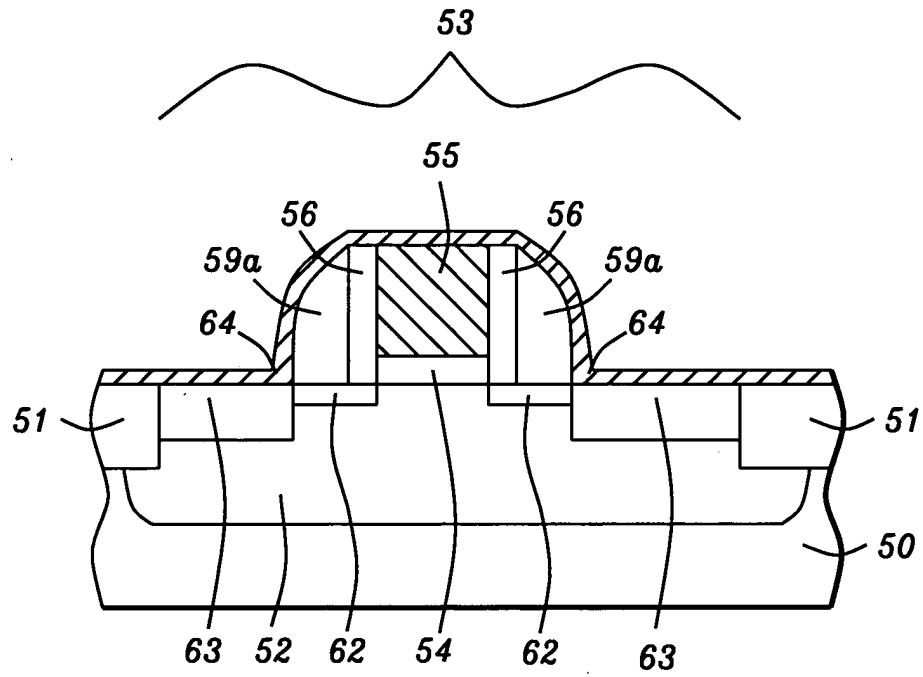


FIG. 4e

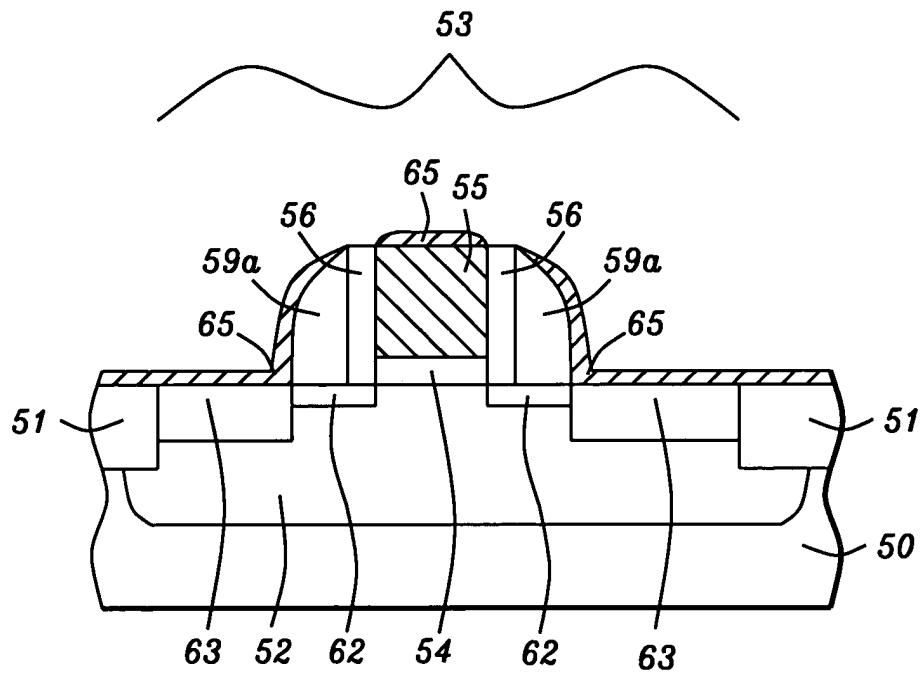


FIG. 4f